Implementation of fuzzy logic control algorithm in embedded microcomputers for dedicated application

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Abstract

Fuzzy logic control algorithm solves problems that are difficult to address with traditional control techniques. This paper describes an implementation of fuzzy logic control algorithm using inexpensive hardware as well as how to use fuzzy logic to tackle a specific control problem without any special software tools. As a case study, hardware implementation of fuzzy control algorithm for online temperature control system is demonstrated using 8-bit microcontroller. The hardware implementation followed by software approach has been discussed. Real time result of fuzzy logic temperature control system is also presented.

Keywords: AVR, Fuzzy, Microcontrollers, embedded.

1. Introduction

Fuzzy techniques have been successfully used in control in several fields, and Engineers and Researchers are today considering fuzzy logic algorithms in order to implement intelligent functions in Embedded Systems. Fuzzy logic controller delivers improved performance over a classical controller has been testified by many researchers. (Nour et al., 2007). Combination of a fuzzy inference system and PID is the better approach to control highly non-linear dynamics process. (Kleanthis et al., 2006). In the past few decades or so, the main area of success with fuzzy logic was in industry control. The application of fuzzy logic allows us to specify the relationship between sensor inputs and actuator outputs using “If...Then...” type of linguistic rules. A fuzzy logic algorithm would be able to translate or interpolate these rules into a nonlinear mapping between sensor input signals and actuator outputs for feedback control (Cox, 1998). Fuzzy logic makes it easy for a human designer to fine tune a control system through trial and success approach. Together with some other approaches such as Artificial Neural Networks, Genetic Algorithm etc., Fuzzy logic considered to be a useful tool for non-model based control system design.

This paper discusses an implementation of a fuzzy inference unit and algorithms for fuzzification, rule evaluation and defuzzification of a fuzzy closed loop control system. Techniques to generate optimized fuzzy based real time code in C, with short development time have being demonstrated using the Atmel AVR Atmega8 microcontroller. We have constructed a stand-alone fuzzy logic controller (FLC) for an online temperature control system. The performance of the Atmega8 for fuzzy-based control is studied.

2. Fuzzy Temperature Controller Hardware Design

FLC consist of temperature control manifested in the form of fuzzy relation between the present temperature to be controlled and the desired set-point temperature (Driankow et al.,1996). The fundamental nature of Fuzzy Control Algorithm is the conditional statement between fuzzy input variable (present furnace temperature) and fuzzy output variable (heater current). A temperature control system consisted of a sensor, actuator, and microcontroller. Fig.1 shows the actual block diagram of fuzzy logic temperature controller.

Fuzzy Logic has been employed to control temperature by varying ON-time of heater. The actual temperature is read and compared with the corresponding to set-point temperature. The error-count is used to trigger the Fuzzy Inference process. This develops an overall power for heater control that maintains the temperature of furnace to the set value.
Temperature is being measured using an integrated circuit temperature sensor LM35, which have an analog output voltage. The output voltage of sensor is linearly proportional to temperature with a gradient of 10mV/ºC and able to operate in the range -55ºC to +150ºC with an accuracy of ± 0.5ºC. The analog output of sensor is given to on-chip Analog to Digital Converter (ADC) of microcontroller. ADC converts an analog voltage into corresponding digital word, which is processed to get the actual physical parameter and then displayed on the LCD module interfaced to the ports of microcontroller. Circuit diagram of the fuzzy logic temperature control system is depicted in Fig. 2.

2.1 Microcontroller Unit: The system circuit diagram shown in Fig.2 has been designed around the ATMEG AVR Atmega8 microcontroller. The microcontroller’s on-chip peripherals like programmable I/O port, Timer and External Rest, RC-oscillator, EEPROM, Power On Reset (POR) are being used to lower the cost and to increase the efficiency and reliability. This makes AVR microcontroller a better choice for such embedded systems. MCU uses calibrated 8 MHz internal RC oscillator for system clock.
2.3 Zero crossing detector: In order to fire Triac exactly at the zero point of the AC cycle, zero crossing detector has been employed. Zero crossing detector generates pulses for every zero crossing of the input AC signal. These pulses are fed to the microcontroller interrupt pin through the opto-coupler. The opto-coupler is used for the isolation of the high voltage AC to the low voltage DC supply at the microcontroller side.

2.4 Heater actuator: The power control device used here is a triac BT136 being least expensive power switch to operate directly on the 110/240V mains. It can withstand a maximum load current typically 5A. Phase angle control technique is employed to control the load power (Philips Semiconductors, Application note). The output power is controlled by the phase delay of the triac drive. This delay is referred to the zero crossing of the line voltage detected by zero crossing detector circuit. The control output from microcontroller port pin is given to opto-coupler MOC3031M to trigger the triac. The triac is driven in quadrants QII and QIII with 60 mA gate current with 100uS pulse width. This pulse width is sufficiently long to insure the triac gets latched at the end of the pulse.

3. Software Implementation of Fuzzy Logic Temperature Controller

3.1 Fuzzification of Input: Fuzzification is the process of changing a real scalar value into a fuzzy value. Fuzzy variables are used to translate real values into fuzzy values. The possible values of a fuzzy variable are not numbers but so-called “linguistic terms”. The input linguistic variables for Fuzzy Logic Temperature Controller (FLTC) express linguistically the difference between the set point and the measured and calculated signals from a temperature sensor. Input to FLTC is Error= (Set point-Temperature sensed). Triangular and trapezoidal membership functions are used to fuzzify the input. For fuzzifier program, it is necessary to determine the range of fuzzy variables related to the crisp inputs. Difference Temperature sensed is restricted to positive values. The following fuzzy sets are used: NEG =negative, SNEG=small negative, ZERO= zero, SPOZ=small positive, POZ= positive. Table 1 shows the Membership function for input linguistic variable.

<table>
<thead>
<tr>
<th>Sr No</th>
<th>Crisp Input Range (Error=Set point - Current Temperature)</th>
<th>Fuzzy Variable Name</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>-15 to -50</td>
<td>NEG</td>
</tr>
<tr>
<td>2</td>
<td>0 to -30</td>
<td>SNEG</td>
</tr>
<tr>
<td>3</td>
<td>-15 to +15</td>
<td>ZERO</td>
</tr>
<tr>
<td>4</td>
<td>0 to 30</td>
<td>SPOZ</td>
</tr>
<tr>
<td>5</td>
<td>15 to 50</td>
<td>POZ</td>
</tr>
</tbody>
</table>

To include the linguistic variable positive (POZ) to a microcontroller, translation of the graphical representation into meaningful code is needed. The following C code fragment gives one example of how to do this. The subprogram “fuzzify_MF” receive the crisp input value, range of fuzzy variables for membership function and returns a degree of membership, scaled between zero and one, indicating the degree to which a given input temperature can be POZ. Similarly, degree for each input membership function is calculated. Fig.3 shows the graphical representation of the membership function for input linguistic variable.

```c
float fuzzify_MF(float x,a,b,c,d) //x=crisp input
{
    float dom;
    if ( x >a && x <b )
    {
        dom=(x-a)/(b – a);
    }
    else if (x>c && x<d)
    {
        dom=(d-x)/(d-c);
    }
    else if (x>=b && x<=c)
    {
        dom=1.0;
    }
    else
    {
        dom=0;
    }
    return dom;
}
```
3.2 Fuzzy Membership Functions for Outputs: The output linguistic variables express linguistically the applied values to the FLTC actuators for temperature control. The output linguistic variables are shown in the Table 2. Present study considered typically one output variable, which is a firing angle. It is necessary to assign fuzzy memberships to output variable, similar to the input variable. The fuzzy sets used for firing angle are as follows: Z = zero, L = large, M = medium, H = high, VH = very high. Fig. 4 shows the graphical representation of the membership function for output linguistic variable.

<table>
<thead>
<tr>
<th>Table 2. Output linguistic variables</th>
</tr>
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<tbody>
<tr>
<td>Sr No</td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td>2</td>
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<tr>
<td>3</td>
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<td>4</td>
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<td>5</td>
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</table>

3.3 Knowledge Representation and Rule block: Once the current values of the input variables are fuzzified, the fuzzy controller continues with the phase of “making decisions,” or deciding what actions to take to bring the temperature to its set-point value. The criteria for this action are minimum time and minimum temperature oscillation. The rule blocks contain the control policy of a Fuzzy Control System. The rules ‘IF’ part describes the situation, for which the rules are designed. The 'THEN' part describes the response of the fuzzy system in this situation. The control policy of heater is structurally formulated in terms of fuzzy rules as shown in table 3.

<table>
<thead>
<tr>
<th>Table 3. Fuzzy rules</th>
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<tbody>
<tr>
<td>Rule No</td>
</tr>
<tr>
<td>1</td>
</tr>
<tr>
<td>2</td>
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<td>3</td>
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<td>4</td>
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<td>5</td>
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</tbody>
</table>
These fuzzy rules can be flexibly modified whenever needed is achieved by a fragment of C program as given below:

```c
struct fuzzy_rule {
    struct MF_label *input1;
    struct MF_label *output;
    float f;
};
struct fuzzy_rule R[Num_rules];
```

For example, rule1 “If error is NEG then firing angle is Z” can be defined as follows:

```c
R[1].input1=&NEG;
R[1].output=&Z;
R[1].f=0; //initializing
```

This process is iterative based on trial and success approach for entire rule base.

### 3.4 Defuzzification

The result of the defuzzification has to be a numeric value that determines the firing angle of the Triac used to drive the heater. There are many ways to perform defuzzification; in this case, crisp output is obtained by weighted average defuzzification technique described by equation (1).

\[
Defuz = \frac{\sum_{i=1}^{5} P[i] \times W[i]}{\sum_{i=1}^{5} W[i]} \quad \ldots (1)
\]

P[i] is the peak value of i\(^{th}\) output membership function. W[i] is the weight associated with i\(^{th}\) rule. The following C code fragment gives one example of how to convert fuzzy variable to crisp output.

```c
int defuzzify(float W1,W2,W3,W4,W5) //receive Degree of Each Membership function
{
    float SM_DOM;
    int OUTPUT;
    SM_DOM = H1 + H2 + H3+H4+H5; //sum of Degree of Membership function
    OUTPUT = ((W1*64935) + (W2*63735) + (W3*60535) + (W4*57735) + (W5*64533)) / (SM_DOM);
    Return(OUTPUT);
}
```

The subprogram “defuzzify” receives the degree of each membership function calculated in fuzzification step and returns defuzzified output, which is used to control the firing angle of triac.

### 4. Results and Discussion

The temperatures in the case study are as follows: set temperature = 40°C; and current temperature = 27°C. Using data logger device real time readings are collected (Nhivekar et al., 2011) and current temperature versus time graph is plotted as shown in Fig. 5. On the graph it can be observed that after 58 sec. temperature set point is achieved and after 62 sec. it become steady around +/- 1°C of set point. For the low cost Embedded System, the real time result is quite acceptable. Defuzzified result of output phase angle verses input error is shown in Fig. 6.

**Figure 5.** Real time result for current Temperature versus Time
The FLTC system program after initializing the all-peripheral calls a heater-driving program and loads an initial heater control count corresponding to the room temperature, Fig. 7 show the flowchart of system program behavior.

The comparison with the defuzzified count corresponding to the set-point fires the rule and accordingly phase delay of the triac drive (firing angle) gets adjust until the set-point temperature is attained. At this stage, defuzzified microcontroller timer counts corresponding to the phase delay of the triac drive is evaluated and the heater power is controlled to maintain the temperature constant. Microcontroller On chip, 16-bit timer is used to adjust the phase delay of the triac. The temperature is displayed and the process of temperature control continues. Table 4 shows the some performance parameters of FLTC system namely Rise time, Settling time, Steady-state error, Algorithm Execution time and memory used to implement the FLTC system on AVR atmega8 microcontroller.

**Table 4. Some performance parameters for FLTC system**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
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<tbody>
<tr>
<td>Rise time</td>
<td>58 sec.</td>
</tr>
<tr>
<td>Settling time</td>
<td>62 sec.</td>
</tr>
<tr>
<td>Steady-state Error</td>
<td>+/- 1°C</td>
</tr>
<tr>
<td>Execution time = 0.6mSec</td>
<td>Total program without LCD (FLA+ADC+PWM etc.)</td>
</tr>
<tr>
<td>Execution time = 9.4mSec</td>
<td>Total program with LCD in 4bit mode (FLA+ADC+PWM+LCD etc.)</td>
</tr>
<tr>
<td>Total code size</td>
<td>2665 words (65.0%)</td>
</tr>
<tr>
<td>Free memory block</td>
<td>1431 words (35%)</td>
</tr>
</tbody>
</table>
4. Conclusions

The fuzzy temperature controller is designed and implemented in 8-bit AVR microcontroller without using any special software tool. Unlike some fuzzy controllers with hundreds, or even thousands, of rules running on computer systems, a unique FLC using a small number of rules and simple implementation is demonstrated to solve a temperature control problem with unknown dynamics or variable time delays commonly found in industry. Also the final hardware is stand-alone system rather than a PC (personal computer/laptop computer) based system that takes control decision based on special software tools running on it and hence the design approach presented in this paper minimizes the total cost of hardware and software design. The control result can be improved by resizing the fuzzy sets and finer tuning for the membership functions.

References


Biographical notes

Mr. Nhivekar G.S. received Master degree in Electronics science with Embedded System specialized from Shivaji University Kolhapur, India in 2008. Presently he is a research fellow in the Department of Electronics, Shivaji University Kolhapur, India. His research area is fuzzy hardware techniques and control applications.
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Received June 2011
Accepted July 2011
Final acceptance in revised form July 2011